Notice of References Cited Application/Control No. 09/932,556 Examiner LeChi Truong Applicant(s)/Patent Under Reexamination GERARD CHAUVEL Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,906,000	05-1999	Abe et al.	711/151
	В	US-4,449,183	05-1984	Flahive et al.	711/151
	ပ	US-4,814,974	03-1989	Narayanan et al.	710/244
	D	US-5,581,722	12-1996	Welland, Robert V.	711/207
	E	US-5,918,160	06-1999	Lysejko et al.	455/74
	F	US-			
	G	US-			
	Н	US-			**
	_	US-			
	J	US-			
	Κ	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0				***	
	Р					
	Q					
	R	13 1807				
	s					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)				
	U	David Eck, xLogicCircuits Lab 2: Memory Circuits, 1997				
	V					
	w					
	x					

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.